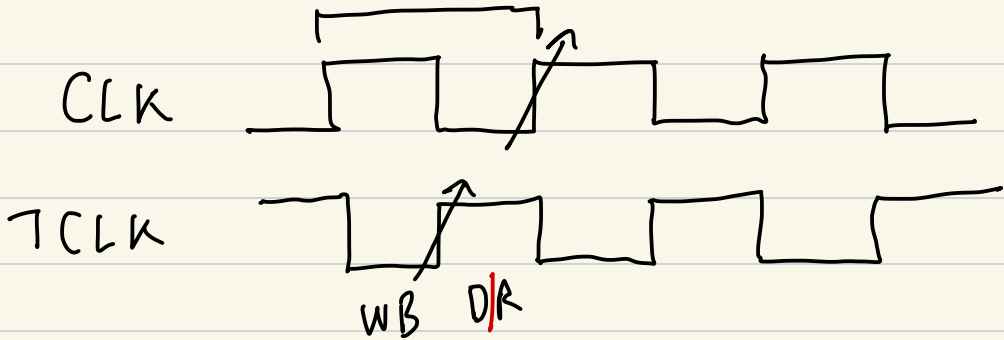


# CS 315-02 Processor Design Hazard Unit

## 4 type of modifications

① Invert clock to Reg File



IF	EX	MEM	WB
	no d	no d	
add a0, a1, a2			addi a1, zero, 3
			↑

addi t0, zero, 7  
beq t0, t1, foo

(2) Forwarding

(3) LD stall / flush

(4) Control Hazard